



## Secondary Reference of TSic™

**TSic 3 0 6 TO92 -30/70**  
 a b c d e

<b>a</b>	<b>Accuracy Type</b>
	1 = ± 0.5°C @ RT; one point calibration
	2 = ± 0.5°C at 80°C range
	3 = ± 0.3°C at 80°C range
	4 = not defined
	5 = ± 0.1°C at 40°C range; limited measuring range (-10°C to 60°C)
	6 = not defined
	7 = ± 0.05°C at 20°C range; limited measuring range (-10°C to 60°C)
<b>b</b>	<b>Bit Size</b>
	0 = 11 Bit
	1 = 14 Bit
<b>c</b>	<b>Output Signal</b>
	1 = analog, 0-1V
	3 = ratiometric, 10% - 90% V <sup>+</sup>
	6 = digital, ZACWire Protocol
<b>d</b>	<b>Housing</b>
	SOP-8 = 150mil, Standard SMT Technology, SOIC-8, after IEC 191-2Q: Type 076E35 B
	SOIC8 = 150mil, Standard SMT Technology, SOIC-8, after IEC 191-2Q
	TO92 = new THT housing (EP3)
	e-line = old THT housing
	KGD = Bare dice at waffle pack
	Wafer = Bare dice at wafer, not sawing
<b>e</b>	<b>Special</b>
	Standard: no value e.g. "250 Hz" for high sampling rate -30/70 = Temperature and tolerance range



INNOVATIVE SENSOR TECHNOLOGY